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10/791,924	03/02/2004	Brian T. Brunn	X-1549 US	3818
²⁴³⁰⁹ XILINX, INC	7590 11/28/2007	EXAMINER		
ATTN: LEGAL DEPARTMENT			LEE, SIU M	
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

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•	Application No.	Applicant(s)
	10/791,924	BRUNN ET AL.
Office Action Summary	Examiner	Art Unit
	Siu M. Lee	2611
The MAILING DATE of this communication Period for Reply	n appears on the cover sheet w	ith the correspondence address
A SHORTENED STATUTORY PERIOD FOR RI WHICHEVER IS LONGER, FROM THE MAILIN - Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication. If NO period for reply is specified above, the maximum statutory p - Failure to reply within the set or extended period for reply will, by s Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	G DATE OF THIS COMMUNI FR 1.136(a). In no event, however, may a n. eriod will apply and will expire SIX (6) MOI statute, cause the application to become A	CATION. reply be timely filed NTHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).
Status		
1) ☐ Responsive to communication(s) filed on 2a) ☐ This action is FINAL . 2b) ☐ 3) ☐ Since this application is in condition for all closed in accordance with the practice uncondition.	This action is non-final. owance except for formal mat	
Disposition of Claims		
4) Claim(s) <u>1-20</u> is/are pending in the application 4a) Of the above claim(s) is/are with 5) Claim(s) <u>16 and 18-20</u> is/are allowed. 6) Claim(s) <u>1-3,5,7,9-11,13-15 and 17</u> is/are 7) Claim(s) <u>4,6,8 and 12</u> is/are objected to. 8) Claim(s) are subject to restriction a	ndrawn from consideration.	
Application Papers		
9) The specification is objected to by the Exa 10) The drawing(s) filed on 14 September 200 Applicant may not request that any objection to Replacement drawing sheet(s) including the control of the	$\frac{7}{2}$ is/are: a) $\boxed{\triangle}$ accepted or b) $\boxed{\triangle}$ or the drawing(s) be held in abeya prection is required if the drawing	nce. See 37 CFR 1.85(a). g(s) is objected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for for a) All b) Some * c) None of: 1. Certified copies of the priority docur 2. Certified copies of the priority docur 3. Copies of the certified copies of the application from the International But * See the attached detailed Office action for a	ments have been received. ments have been received in A priority documents have beer ureau (PCT Rule 17.2(a)).	Application No received in this National Stage
Attachment(s)	م □ دغه	
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-94) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date 	8) Paper No	Summary (PTO-413) (s)/Mail Date Informal Patent Application

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DETAILED ACTION

Response to Arguments

- 1. Applicant's arguments, see page 6, filed 9/14/2007, with respect to objection to the drawings have been fully considered and are persuasive. The objection of figure 4, 5, and 6 has been withdrawn.
- 2. Applicant's arguments with respect to claims 1-20 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 112

3. Claim 17 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 16 recites a method of shaping the pulse wherein the shaping of the pulse results in a modified pulse that has zero crossings located substantially at bit edge within the sequence of bit periods. The examiner interpret claim 16 describes the embodiment 2 as discloses in figure 5A of the instant application. Claim 17 depend on claim 16 and it recites the limitation "the shaping of the pulse results in a modified pulse that has zero crossings located substantially at bit edge of each bit period within the sequence of bit periods except those bit edges immediately adjacent to the bit period in which the pulse is substantially located". The examiner interpret claim 17 describes the embodiment 1 as discloses in figure 5A. Since claim 17 depends on claim 16,

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therefore, claim 17 mixes the embodiment 1 and the embodiment 2 of the instant application and it is unclear which embodiment claim 17 is directed to.

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 1, 2, 7, 9, 11, and 13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lenosky (US 6,956,917 B2) in view of Brianti et al. (US 6,246,731 B1).
 - (1) Regarding claims 1 and 11:

Lenosky discloses an apparatus comprising:

a filter tap coefficient module that provides a plurality of filter tap coefficients (computation of the coefficients is accomplished by the microcontroller 206 in figure 2, column 12, lines 33-34);

a filter (filter as discloses in figure 4) that includes a plurality of filter taps such that each filter tap is adjusted according to one filter tap coefficient of the plurality of filter tap coefficients (the filter in figure 4 comprises a plurality of coefficient multipliers multiplying the filter tap coefficients with s[n] fro estimating the current data symbol based on the combined output from the precursor and postcursor filter, column 12, lines 5-18);

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wherein the filter is enabled to modify a pulse in a communication channel, wherein the modified pulse is located within a sequence of bit periods (as shown in figure 3, the received bit is located within a sequence of bit period, column 6, lines 14-18).

Lenosky fails to disclose wherein the modified pulse has zero crossings located substantially at bit edges of each bit period within the sequence of bit periods except those bit edges immediately adjacent to a bit period in which the pulse is substantially located.

However, Brianti et al. discloses wherein the modified pulse has zero crossings located substantially at bit edges of each bit period within the sequence of bit periods except those bit edges immediately adjacent to a bit period in which the pulse is substantially located (the second waveform (m=1) in figure 1 with the zero crossings at each bit period within the sequence of bit period except those bit edge immediately adjacent to a bit period in the center).

It is desirable wherein the modified pulse has zero crossings located substantially at bit edges of each bit period within the sequence of bit periods except those bit edges immediately adjacent to a bit period in which the pulse is substantially located because it enforce the spectral properties and allows a controlled amount of intersymbol interference (column 1, lines 43-44). Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to employ the teaching of Brianti et al. in the apparatus of Lenosky to improve the performance of the apparatus.

(2) Regarding claims 2 and 13:

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Lenosky discloses the filter tap coefficient module (microcontroller 206 in figure 2) calculates the plurality of filter tap coefficients in real time based on current updated characteristic information of the communication channel that communicatively couples a transmitter and a receiver (the microcontroller apply the calculated channel response to obtain filter coefficients for the equalizer, in other words, the equalizer uses the channel response and to equalize the received signal s(t), column 11, lines 45-51, computation of the coefficients is accomplished by the microcontroller 206 with a routine that receives the current estimate of the channel responses as an input, and return the optimal coefficient as an output, column 12, lies 33-38, one feature of the present invention is the ability to update the estimate of the tap weights based on changes in the estimate of the channel response, column 15, lines 3-5).

(3) Regarding claim 7:

Lenosky discloses that the plurality of filter taps includes 3 filter taps; and the plurality of filter tap coefficients includes 3 corresponding filter tap coefficients (the actual number of coefficients in the precursor and the postcursor section of the filter are determined by a designer before construction of the filter, column 12, lines 28-30, the number of tap and tap coefficients are actually a design choice of how many is to be used).

(4) Regarding claim 9:

Lenosky discloses that the filter is implemented within a receiver that is communicatively coupled to a transmitter via the communication channel (as discloses

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in figure 2, an adaptive channel-compensating equalizer received a signal s(t) transmitted from the transmitter as discloses in figure 1(a) and (b)).

6. Claims 3 and 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lenosky (US 6,956,917 B2) in view of Brianti et al. (US 6,246,731 B1) as applied to claim 1 above, and further in view of Gruber (US 5,249,150).

Lenosky and Brianti et al. disclose all the subject matter as discussed in claim 1, Lenosky further disclose the filter tap coefficient module calculates the plurality of filter tap coefficients based on predetermined characteristic information of the communication channel that communicatively couples a transmitter and a receiver (these initial values fro the coefficients are predetermined by the computational logic, and could, as one example, simply be set to zero, column 10, lines 54-57).

Lenosky and Brianti et al. fails to disclose calculates the plurality of filter tap coefficients offline.

However, Gruber discloses calculates the plurality of filter tap coefficients offline (The coefficients of the optimal filter may be calculated offline, e.g. in advance, and be stored in a memory, e.g. a ROM, PROM or RAM, column 7, lines 55-58).

It is desirable to calculate the plurality of filter tap coefficients offline because this allows certain portions of the terminal to be powered up for a shorter period of time, with unnecessary circuitry such as the front-end circuitry being powered down to reduce power consumption. Therefore, it would have been obvious to one of ordinary skill in

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the art at the time of invention to employ the teaching of Gruber in the apparatus of Lenosky and Brianti et al. to reduce the power consumption of the system.

7. Claims 5 and 15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lenosky (US 6,956,917 B2) in view of Brianti et al. (US 6,246,731 B1) as applied to claim 1 above, and further in view of Veeneman et al. (US 4,852,169).

Lenosky and Brianti et al. disclose all the subject matter as discussed in claim 1 except wherein a sum of absolute values of each filter tap coefficient of the plurality of filter tap coefficient is substantially equal to one.

However, Veeneman et al. disclose a method to normalize the filter coefficient so that the sum of all the coefficients is equal to one (column 7, lines 47-48).

It is desirable to have a sum of absolute values of each filter tap coefficient of the plurality of filter tap coefficient is substantially equal to one because it can reduce the number of filter elements, particularly in the number of multipliers. Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to employ the teaching of Veeneman et al. in the apparatus of Lenosky and Brianti et al. to simplify the system.

8. Claim 10 is rejected under 35 U.S.C. 103(a) as being unpatentable over Lenosky (US 6,956,917 B2) in view of Brianti et al. (US 6,246,731 B1) as applied to claim 1 above, and further in view of Kohlenberg et al. (US 3,876,941).

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Lenosky and Brianti et al. disclose all the subject matter as discuss in claim 1,

Lenosky further discloses that the transmitter and the receiver are communicatively

coupled via the communication channel (fig. 1 discloses a model of the transmitter and

a receiver communicatively coupled via the communication channel, column 3, lines 43
56).

Lenosky and Brianti et al. fail to disclose wherein the filter is implemented in a distributed manner part in a transmitter and part in a receiver.

However, Kohlenberg et al. discloses a filter network that is distributed between transmitter and receiver (abstract, lines 8-12).

It is desirable to disclose wherein the filter is implemented in a distributed manner part in a transmitter and part in a receiver because it can simplify and improve the effectiveness of the match filter communication (column 2, lines 53-55). Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to employ the teaching of Kohlenberg et al. in the apparatus of Lenosky and Brianti et al. to improve the performance of the apparatus.

Allowable Subject Matter

- 9. Claims 16,18-20 are allowed.
- 10. Claims 4, 6, 8, 12 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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Conclusion

11. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Kahlman et al. (US 5,586,144) discloses a receiving arrangement including a variable equalizer, which variable equalizer is controlled on the basis of one or more signal portions produced by the variable equalizer. Cullum (US 4,694,468) discloses an apparatus useful in channel equalization adjustment. Markman et al.(US 7,038,730 B2) discloses a matched pulse shaping filter.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Siu M. Lee whose telephone number is (571) 270-1083. The examiner can normally be reached on Mon-Fri, 7:30-4:00 with every other Friday off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chieh Fan can be reached on (571) 272-3042. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Siu M Lee Examiner Art Unit 2611 11/25/2007

CHIEH M. FAN
SUPERVISORY PATENT EXAMINER